

Electronic Acknowledgement Receipt

EFS ID:	2149053
Application Number:	10757788
International Application Number:	
Confirmation Number:	5462
Title of Invention:	Method and apparatus for the automatic correction of faulty wires in a logic simulation hardware emulator / accelerator
First Named Inventor/Applicant Name:	Thomas Michael Gooding
Customer Number:	30206
Filer:	James R. Nock/Deb Peterson
Filer Authorized By:	James R. Nock
Attorney Docket Number:	ROC920030335US1
Receipt Date:	31-AUG-2007
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Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	yes
Payment was successfully received in RAM	\$ 790
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part/.zip	Pages (if appl.)
1	Request for Continued Examination (RCE)	ROC920030335US1_Reque stforContinuedExam_08-31- 07.pdf	742242 363136e41d2b349b0c263d25011216b 3b1605395	no	3
Warnings:					
Information:					
2	Amendment Submitted/Entered with Filing of CPA/RCE	ROC920030335US1_Prelimi naryAmendment_08-31-07.p df	608528 b61a0d65d21ade23a72f9069db3edfe23 d4f4a2	no	16
Warnings:					
Information:					
3	Fee Worksheet (PTO-06)	fee-info.pdf	8235 6b3a8ff3aae669774fd87a0cf332a7fd d4f43e	no	2
Warnings:					
Information:					
Total Files Size (in bytes):				1359005	
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